

Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheet	1	of	4	Attorney Docket Number	026517-0035P
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)			
		US- 5,107,257	04-21-1992	Fukuda	
		US- 5,265,257	11-23-1993	Simcoe et al.	
		US- 5,469,433	11-21-1995	McAuley	
		US- 5,546,546	08-13-1996	Bell et al.	
		US- 5,634,006	05-27-1997	Baugher et al.	
		US- 5,745,913	04-28-1998	Pattin et al.	
		US- 6,119,183	09-12-2000	Briel et al.	
		US- 6,141,355	10-31-2000	Palmer et al.	
		US- 6,141,713	10-31-2000	Kang	
		US- 6,198,724	03-06-2001	Lam et al.	
		US- 6,199,131	03-06-2001	Melo et al.	
		US- 6,212,611	04-03-2001	Nizar et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 00/29956 A	05-25-2000	Infineon		
		JP 4238133	12-26-2008	Sonics, Inc.		
		JP 2000-250853	09-14-2000	NEC Corp.		
		JP H09-138774	05-27-1999	Tandem Computers Inc.		
		JP H11-250004	09-17-1999	NEC Corp.		
		WO 2003/034242	04-24-2003	Sonics, Inc.		
		WO 01/93477 A1	12-06-2001	Sonics, Inc.		
		WO 2005/045727	05-19-2005	Sonics, Inc.		
		10-2006-7010659	05-30-2006	Sonics, Inc.		

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<p>Substitute for Form 1449/PTO</p> <p>INFORMATION DISCLOSURE</p> <p>STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>					Complete if Known			
					Application Number		10/698,905	
					Filing Date		October 31, 2003	
					First Named Inventor:		Wolf-Dietrich Weber	
					Art Unit		2192	
					Examiner Name			
					Rutten, James D.			
Sheet	2	of	4	Attorney Docket Number	026517-0035P			
U.S. PATENT DOCUMENTS								
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		Number-Kind Code ² (If known)						
		US-	6,215,789	04-10-2001	Keenan et al.			
		US-	6,215,797	04-10-2001	Fellman et al.			
		US-	6,266,718	07-24-2001	Klein			
		US-	6,430,156	08-06-2002	Park et al.			
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		US-	6,721,325	04-13-2004	Duckering et al.			
		US-	6,862,265	03-01-2005	Appala et al.			
		US-	6,976,106	12-13-2005	Tomlinson et al.			
		US-	2001/0026535	10-04-2001	Amou et al.			
		US-						

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				First Named Inventor:	Wolf-Dietrich Weber
				Art Unit	2192
				Examiner Name	Rutten, James D.
				Attorney Docket Number	026517-0035P
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		Weber, Wolf-Dietrich, et al., "Enabling Reuse via an IP Core-centric Communications Protocol: Open Core Protocol", In Proceedings of the IP 2000 System-on-Chip Conference Mar. 2000, pp. 1-5.			
		PCT International Search Report for PCT/US2004/035863, Int'l filing Oct. 27, 2004, mailed Aug. 9, 2005, 9 pages.			
		Wingard, Drew, "Sonics SOC Integration Architecture," Sonics, Inc., 1500 presentation, 1999, 25 pages, www.00P-IP.org. Kamas, Alan, "The SystemC OCP Models; An Overview of the SystemC Models for the Open Core Protocol," NASCUG, 2004, 30 pages.			
		Wingard, Drew, "Socket-Based Design Using Decoupled Interconnects," Sonics, Inc., 30 pages, downloaded Jun. 14, 2004, www. OCP-IP.org.			
		Haverinen, Anssi, "SystemCTM Based SoC Communication Modeling for the OCP Protocol," White Paper, Oct. 2002, V1.0, 39 pages.			
		Wingard, Drew, "Tiles-An Architectural Abstraction for Platform-Based Design," Perspective article 2, EDA Vision, Jun. 2002, 3 pages, www.edavision.com.			
		Weber, Wolf-Dietrich, "Efficient Shared DRAM Subsystems for SOCs," Sonics, Inc., 2001, 6 pages.			
		"Open Core Protocol Specification," OCP International Partnership, Release 1.0, 2001, 2 pgs.			
		Wingard, Drew PhD., "Integrating Semiconductor IP Using uNetworks," ASIC Design, Jul. 2000 electronic engineering, 3 pages.			
		Wingard, Drew, "Tiles: the Heterogeneous Processing Abstraction for MPSoC," Sonics, Inc., Smart Interconnect IP, 2004, 35 pages, www.00P-IP.org.			
		Chou, Joe, "System-Level Design Using OCP Based Transaction-Level Models," presentation, Denali MemCom Taiwan 2005, OCP International Partnership, 23 pages.			

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		Wingard, Drew, "A Non-Blocking Intelligent Interconnect for AMBA-Connected SoCs," Sonics, Inc., CoWare Arm Developer's Conference, Oct. 6, 2005, 39 pages.			
		Weber, Wolf-Dietrich, et. al., "A Quality-of-Service Mechanism for Interconnection Networks In System-on-Chips," 1530-1591/05, 2005 IEEE, 6 pages.			
		Casini, Phil, "Measuring the Value of Third Party Interconnects," Sonics, Inc., White Paper, 2005, 11 pages, www.sonicsinc.com .			
		European Search Report for International Application No. EP 02 71 3653, mailed on May 29, 2006, pp. 3 total.			
		First Exam Report for European Application No. 04796678.3 dated 12/11/06, 6 pages.			
		Second Exam Report for European Application No. 04796678.3 dated 12/14/07, 6 pages.			
		Third Exam Report for European Application No. 04796678.3 dated 3/27/09, 4 pages.			
		International Search Report for International Patent Application No. PCT/US2004/035863 dated 8/9/05, 7 pages.			
		International Preliminary Report on Patentability and Written Opinion for International Patent Application No. PCT/US2004/035863 dated 5/1/06, 11 pages.			

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